

Atty. Docket No. CPAC 1029-5
Appl. No. 10/681,583

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PATENT

OCT 19 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Marcos Kamezos)
Application No.: 10/681,583) Examiner: Alonzo CHAMBLISS
Filed: October 8, 2003) Group Art Unit: 2814
Title: (Amended) Semiconductor multi-package) Date: 19 October 2005
module having inverted second package)
stacked over die-up flip-chip ball grid array)
(BGA) package)

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, at the Central Fax No. 571-273-8300 on 19 October 2005.

Signed

Paula Faulk Hurley
Paula Faulk Hurley

RESPONSE TO RESTRICTION REQUIREMENT
AND PRELIMINARY AMENDMENT

Dear Sir:

Responsive to the Office action mailed September 22, 2005 (Restriction only), Applicant elects:

Group I, Claims 1 - 20, 37 and 38
for prosecution in this Application.

Amendments to the Specification begin on page 2 of this paper.

There are no amendments to the claims. A Listing of Claims is included for the Examiner's convenience, beginning on page 3 of this paper.

Remarks begin on page 8 of this paper.